

PATENT ABSTRACTS OF JAPAN

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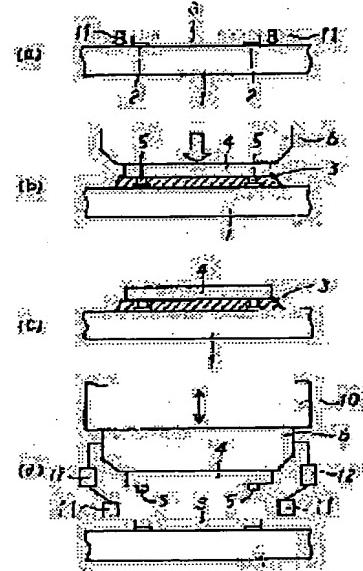
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(54) METHOD AND DEVICE FOR MOUNTING SEMICONDUCTOR CHIP

(57)Abstract:

PURPOSE: To improve the mounting reliability of the method and device for mounting a semiconductor chip with its face downward using a thermosetting adhesive.

CONSTITUTION: An adhesive 3 is applied to the semiconductor chip mounting part of a substrate 1, the adhesive 3 is pre-heated by the local heating of the substrate 1, a semiconductor chip 4 which is heated to the thermosetting temperature of the adhesive 3 is pressed to the semiconductor chip mounting part of the substrate 1, and then the adhesive 3 is cured. A semiconductor chip suction means is provided on the lower surface of a pressure head 6 for heating the semiconductor chip 4 to the thermosetting temperature of the adhesive 3, a heating body 11 is lowered from the head 6 by a vertically movable means 12, and the heating body 1 contacts the substrate 1 before the semiconductor chip 4 contacts the adhesive 3.



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CLAIMS

[Claim(s)]

[Claim 1] Thermosetting adhesive (3) It uses and is a semiconductor chip (4). Substrate (1) It faces carrying out face down mounting. This substrate (1) They are these adhesives (3) to the semiconductor chip mounting section. It sticks. This substrate (1) They are these adhesives (3) by local heating. Preheating is performed and it is this semiconductor chip (4). This substrate (1) It presses in the semiconductor chip mounting section, and they are these adhesives (3). The mounting approach of the semiconductor chip characterized by heating to curing temperature and stiffening it.

[Claim 2] Said substrate (1) Local heating is said semiconductor chip (4). Heating object which contacts the perimeter of the mounting section (11 31) The mounting approach of the semiconductor chip according to claim 1 characterized by being made.

[Claim 3] Said substrate (1) The heating object (11) which carries out local heating is said semiconductor chip (4). Said adhesives (3) This substrate heated to curing temperature (1) Heating sticking-by-pressure head turned and pressed (6) The mounting approach of the semiconductor chip according to claim 2 characterized by being heated.

[Claim 4] Said substrate (1) The heating object (31) which carries out local heating is this substrate (1). The mounting approach of the semiconductor chip according to claim 2 characterized by building in the heater which carries out local heating.

[Claim 5] A heating object (11) according to claim 2 is said heating sticking-by-pressure head (6). It hangs free [vertical movement] and is this heating sticking-by-pressure head (6). Said heated semiconductor chip (4) Said adhesives (3) It precedes touching and this heating object (11) is this substrate (1). The mounting approach of the semiconductor chip according to claim 2 characterized by touching.

[Claim 6] Thermosetting adhesive (3) It uses and is a semiconductor chip (4). Substrate (1) It faces carrying out face down mounting. These adhesives (3) It is a semiconductor chip (4) to curing temperature. Said heating sticking-by-pressure head to heat (6) In an inferior surface of tongue, it is this semiconductor chip (4). A means to adsorb is established. This semiconductor chip (4) Said heating object which contacts said perimeter of the mounting section (11 31) The means (12) which can be moved up and down is minded and it is this heating sticking-by-pressure head (6). It hangs. This heating sticking-by-pressure head (6) This semiconductor chip to which it stuck (4) These adhesives (3) Before touching, this heating object (11) is this substrate (1). Mounting equipment of the semiconductor chip characterized by constituting so that it may contact.

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DETAILED DESCRIPTION

[Detailed Description of the Invention]

[0001]

[Industrial Application] This invention relates to the mounting approach of a semiconductor chip, the mounting

equipment for it and the method of making a semiconductor chip mount in a glass substrate especially using a heating sticking-by-pressure head, and its equipment.

[0002] Method ** which carries out face down mounting of the semiconductor chip at a glass substrate [finishing / a panel configuration], using a chip-on glass (COG) method, i.e., adhesives, as the mounting approach of the semiconductor chip used for the product, for example, a method of mounting the semiconductor chip for a panel drive in the liquid crystal display panel filled up with liquid crystal between the glass substrates of a pair, came to be adopted with small [of product size], and thin-shape-izing.

[0003]

[Description of the Prior Art] Drawing 7 is the explanatory view of the conventional approach which carries out face down mounting of the semiconductor chip at the circuit board.

[0004] Drawing 7 R> 7 which shows the general approach of carrying out face down mounting of the semiconductor chip at the circuit board using thermosetting adhesive (a) – (c). It sets. Adhesives 3 are stuck on the front face of the circuit board 1 in which the terminal 2 was formed. many conductors — After carrying out alignment of the many bumps 5 and the terminal 2 which were formed in the semiconductor chip 4, The bump 5 of a semiconductor chip 4 is pressed for the terminal 2 of the circuit board 1 using the heating sticking-by-pressure head 6, after about 20 to 30 seconds passed and adhesives 3 have hardened, a head 6 is removed, and face down mounting of a semiconductor chip 4 is completed.

[0005] In the mounting approach of this semiconductor chip 4, it is pushed away by the thrust of a semiconductor chip 4 by the adhesives 3 between the bump 5 who projects from the body of a semiconductor chip 4, and the terminal 2 which projects from the semiconductor chip loading side of the circuit board 1, and the hardened adhesives 3 maintain the mechanical junction to the circuit board 1 and a semiconductor chip 4 while maintaining the electrical installation of a bump 5 and a terminal 2.

[0006] Generally, adhesives 3 are thermosetting, for example, stiffen the thermosetting adhesive 3 of an epoxy system at 170 degrees C – about 200 degrees C. The circuit board 1 is then heated through the semiconductor chip 4 → bump 5 → terminal 2 and the semiconductor chip 4 → adhesives 3.

[0007]

[Problem(s) to be Solved by the Invention] In said mounting approach of a semiconductor chip 4, the circuit board 1 is a glass substrate, and when the glass substrate 1 constitutes a liquid crystal display panel, the deviation film put on the liquid crystal and the glass substrate 1 with which the display panel was filled up cannot be heated at 200 degrees C or more. Therefore, when carrying out face down mounting of the semiconductor chip 4, the glass substrate 1 was a room temperature.

[0008] Therefore, a temperature gradient generates the adhesives 3 hardened with heating for 20 seconds – about 30 seconds through a semiconductor chip 4 between the part which touches a semiconductor chip 4, and the part which touches a glass substrate 1.

[0009] In drawing 8 which shows the temperature gradient of the adhesives 3 at the time of mounting of a semiconductor chip 4, it is the temperature rise property of a part that a continuous line A touches a semiconductor chip 4, and the temperature rise property of a part that a continuous line B touches a glass substrate 1, and when about 30 seconds pass and adhesives 3 harden, the temperature of the adhesives 3 of the part which touches a substrate 1 becomes lower about 20–30 degrees C than the part which touches a semiconductor chip 4.

[0010] Said temperature gradient of adhesives 3 causes the fluid unevenness of adhesives 3, and hardening advance unevenness, and as shown in drawing 9 as a result, air bubbles 7 tend to produce it inside the adhesives 3 which completed hardening. These air bubbles 7 made the dependability over mounting of a semiconductor chip 4 fall, and caused connection interrupt of a semiconductor chip 4 at the time of an accelerated test.

[0011] In addition, although the magnitude of the semiconductor chip 4 mounted in a liquid crystal display panel is the present and about 3mmx17mm–1mmx10mm and said temperature gradient of the adhesives 3 which used the small semiconductor chip 4 becomes comparatively small, air bubbles 7 cannot be lost.

[0012]

[Means for Solving the Problem] Drawing 1 is the explanatory view of the basic configuration of this invention. when face down mounting of the semiconductor chip is carried out at a substrate using thermosetting adhesive, this invention approach it is made air bubbles not produce in the hardened adhesives is shown in drawing 1 (a) — as — the conductor for semiconductor chip mounting — the part of the glass substrate 1 which formed the terminal 2 and stuck the thermosetting adhesive 3 for semiconductor chip mounting, i.e., the periphery of the semiconductor chip mounting section, heats with the heating object 11, and preheating of adhesives 3 performs

through a glass substrate 1.

[0013] Subsequently, after performing alignment of the bump 5 and terminal 2 which were formed in the semiconductor chip 4, as shown in drawing 1 (b), the heating sticking-by-pressure head 6 which adsorbs a semiconductor chip 4 on the inferior surface of tongue, and heats a semiconductor chip 4 to the curing temperature of adhesives 3 is used, and the bump 5 of a semiconductor chip 4 is pressed for the terminal 2 of a substrate 1.

[0014] Then, if a head 6 is removed after adhesives 3 harden, as shown in drawing 1 (c), face down mounting of a semiconductor chip 4 will be completed. As the example of a basic configuration of the semiconductor chip mounting equipment important section equipped with the preheating object 11 of a substrate 1 is shown in drawing 1 (d) When the substrate preheating object 11 hangs through the means (for example, spring) 12 which can be moved up and down and a head 6 is made to ***** by actuation of the pressurization cylinder 10 from the heating sticking-by-pressure head 6 which adsorbs a semiconductor chip 4 on the inferior surface of tongue, After the heating object 11 heated by the head 6 touches a substrate 1, the amount of suspension of the heating object 11 is set up so that the semiconductor chip 4 heated by the head 6 may touch adhesives 3.

[0015]

[Function] As explained above, after, as for this invention, the substrate 1 with which the heating object 11 heated the semiconductor chip mounting section (necessary part) of a substrate 1, and local heating was carried out carries out preheating of the adhesives 3, a semiconductor chip 4 heats adhesives 3 to the curing temperature.

[0016] Therefore, when carrying out face down mounting of the semiconductor chip 4 at the glass substrate 1 which constitutes, the substrate 1, for example, the liquid crystal display panel, which cannot heat the whole, the temperature gradient at the time of hardening of adhesives 3 can be controlled, and the air bubbles 7 generated in the conventional approach can be lost now.

[0017] Consequently, high dependability can be secured now to face down mounting of a semiconductor chip 4.

[0018]

[Example] The explanatory view of the main configurations of equipment [in / in the explanatory view of the main configurations of equipment / in / in drawing 2 / the 1st example of this invention /, the explanatory view of the equipment which shows drawing 3 to drawing 2 of operation, and drawing 4 / the 2nd example of this invention], the explanatory view of the substrate preheating object concerning the 3rd example of this invention in drawing 5, and drawing 6 are the explanatory views of the main configurations of the equipment in the 4th example of this invention.

[0019] In drawing 2 , the inferior surface of tongue of the pressurization cylinder 10 moving up and down is equipped with the heating sticking-by-pressure head 6 which built in the heater. To the inferior surface of tongue of a head 6, the inhalation-of-air hole (adsorption means) which carries out vacuum adsorption of the semiconductor chip 4 carries out opening, and the frame type head 14 for substrate preheating surrounding a semiconductor chip 4 is formed in the side face of a head 6 on it.

[0020] The head 14 for preheating consists of heat-resistant India rubber (means which can be moved up and down) 17 to which a cross section is contained in the KO typeface of the frame type base 15 of the KO typeface cross section where a KO typeface, i.e., downward opening, narrows mostly, the substrate heating object 16 with which the upper part fits in in the KO typeface of a base 15, and the lower limit section projects from a base 15, and a base 15, and always energizes the heating object 16 caudad. In addition, replacement, other elastic bodies, for example, coiled spring, is possible for India rubber 17.

[0021] The heating object 16 which is equivalent to the heating object 11 of drawing 1 , and does not contain a heater comes to be heated by about 150 degrees C by the heating sticking-by-pressure head 6 through a base 15. drawing 3 — setting — many conductors — the sheet-like adhesives 3 are stuck on the front face of the glass substrate 1 in which the terminal 2 was formed (80 degrees C, about 1 second), and after carrying out alignment of the many bumps 5 and the substrate terminal 2 which were formed in the semiconductor chip 4, the inferior surface of tongue of the heated substrate heating object 16 is contacted on the front face of a substrate 1 only about dozens seconds by ***** of the pressurization cylinder 10.

[0022] Then, the heat of the heating object 16 spreads to a substrate 1, and it comes to be heated by the suitable about temperature which is extent to which hardening of adhesives 3 does not progress the semiconductor chip mounting section of a substrate 1 remarkably, for example, 120 degrees C.

[0023] Subsequently, the pressurization cylinder 10 is made to descend further so that India rubber 17 may be compressed, and face down mounting of a semiconductor chip 4 is completed, without generating air bubbles in

adhesives 3, if the semiconductor chip adsorption power of a head 6 is canceled and the pressurization cylinder 10 is raised, after a bump 5 makes it maintain about dozens seconds where a terminal 2 is contacted and stiffens adhesives 3.

[0024] In drawing 4, the metallic ornaments (heating object) 21 equivalent to the heating object 11 of drawing 1 hang to an inferior surface of tongue in the side face of the heating sticking-by-pressure head 6 which carries out vacuum adsorption of the semiconductor chip 4. And the metallic ornaments 21 by which the long hole 22 which has die length was formed in the vertical direction can hang on the side face of a head 6 according to the *** 23 into which a long hole (means which can be moved up and down) 22 fits free [vertical movement], and only the die length of a long hole 22 can move up and down freely with the weight of itself.

[0025] And the metallic ornaments 21 heated by the sticking-by-pressure head 6 enable face down mounting of the semiconductor chip 4 which does not produce air bubbles in adhesives 3 like the heating object 16. In drawing 5, the sheet 31 for glass substrate local heating concerning this invention approach (heating object) sandwiches the exoergic sheet by energization with the insulating film of a pair, and carries and uses it for the semiconductor chip mounting section of a glass substrate 1.

[0026] The example of drawing 6 equips the sticking-by-pressure head 6 with a sheet 31, and are that vertical movement [sheet / 31] is free and an example of a configuration which carries out preheating of the adhesives 3 through a substrate 1, and was made to carry out face down mounting of the semiconductor chip 4 by vertical movement of a head 6.

[0027] From the method which carries out preheating of the attachment adhesives 3 of a substrate 1 by the heating object 16 or metallic ornaments 21 using the heat transmission from the heating sticking-by-pressure head 6, the equipment of drawing 6 which is the example which hung from the side face of the sticking-by-pressure head 6, and equipped with the sheet 31 the metallic ornaments 21 of the pair which can move up and down freely has the good preheating effectiveness of adhesives 3, namely, can shorten the duration of preheating.

[0028]

[Effect of the Invention] This invention loses to a substrate the air bubbles of the adhesives which carried out face down mounting of the semiconductor chip, and enabled it to secure high dependability to face down mounting, as explained above.

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DESCRIPTION OF DRAWINGS

[Brief Description of the Drawings]

[Drawing 1] The explanatory view of the basic configuration of this invention

[Drawing 2] The explanatory view of the main configurations of the equipment in the 1st example of this invention

[Drawing 3] The explanatory view of the equipment shown in drawing 2 of operation

[Drawing 4] The explanatory view of the main configurations of the equipment in the 2nd example of this invention

[Drawing 5] The explanatory view of the substrate preheating object concerning the 3rd example of this invention

[Drawing 6] The explanatory view of the main configurations of the equipment in the 4th example of this invention

[Drawing 7] The explanatory view of the conventional approach which carries out face down mounting of the semiconductor chip

[Drawing 8] Thermal mapping of the adhesives at the time of semiconductor chip mounting by the conventional approach

[Drawing 9] The explanatory view of the air bubbles generated inside the adhesives by the conventional approach

[Description of Notations]

- 1 Semiconductor Chip Mounting Substrate
 - 2 Conductor -- Terminal
 - 3 Thermosetting Adhesive
 - 4 Semiconductor Chip
 - 5 Bump
 - 6 Heating Sticking-by-Pressure Head
 - 11 16 Heating object which heats a substrate part
 - 12 Means Which Can be Moved Up and Down
 - 17 India Rubber (Means Which Can be Moved Up and Down)
 - 21 Metallic Ornaments (Heating Object)
 - 22 Long Hole (Means Which Can be Moved Up and Down)
 - 31 Heating Sheet (Heating Object)
-

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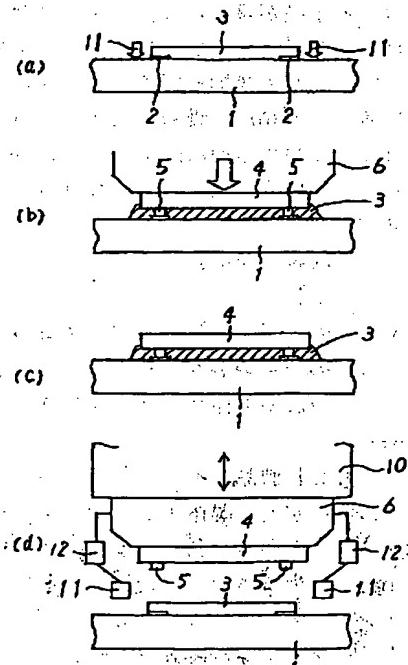
(54) 【発明の名称】 半導体チップの実装方法と実装装置

(57) 【要約】 第一実装部と第二実装部による複数回の実装

【目的】 熱硬化性接着剤を用いて半導体チップをフェイスダウン実装する方法とそのための装置に関し、実装の信頼性向上させる。

【構成】 基板1の半導体チップ実装部に接着剤3を貼り着し、基板1の局部加熱によって接着剤3の予備加熱を行い、接着剤3の硬化温度に加熱した半導体チップ4を基板1の半導体チップ実装部に押圧し、接着剤3を硬化させる。接着剤3の硬化温度に半導体チップ4を加熱する圧着ヘッド6の下面には半導体チップ吸着手段が設けられ、加熱体11が上下動自在手段12を介してヘッド6より垂下し、半導体チップ4が接着剤3に接する前に加熱体11が基板1に当接するように構成する。

本発明の基本構成の説明図



(2)

【特許請求の範囲】

【請求項 1】 熱硬化性接着剤(3)を用いて半導体チップ(4)を基板(1)にフェイスダウン実装するに際し、該基板(1)の半導体チップ実装部に該接着剤(3)を貼着し、該基板(1)の局部加熱によって該接着剤(3)の予備加熱を行い、該半導体チップ(4)を該基板(1)の半導体チップ実装部に押圧し、該接着剤(3)を硬化温度に加熱して硬化させること、を特徴とする半導体チップの実装方法。

【請求項 2】 前記基板(1)の局部加熱は、前記半導体チップ(4)実装部の周囲に当接する加熱体(11, 31)によりなされること、を特徴とする請求項 1 記載の半導体チップの実装方法。

【請求項 3】 前記基板(1)を局部加熱する加熱体(11)が、前記半導体チップ(4)を前記接着剤(3)の硬化温度に加熱した該基板(1)に向けて押圧する加熱圧着ヘッド(6)により加熱されること、を特徴とする請求項 2 記載の半導体チップの実装方法。

【請求項 4】 前記基板(1)を局部加熱する加熱体(31)が、該基板(1)を局部加熱するヒータを内蔵すること、を特徴とする請求項 2 記載の半導体チップの実装方法。

【請求項 5】 請求項 2 記載の加熱体(11)が前記加熱圧着ヘッド(6)より上下動自在に垂下し、該加熱圧着ヘッド(6)により加熱された前記半導体チップ(4)が前記接着剤(3)に接するのに先立つて該加熱体(11)が該基板(1)に接すること、を特徴とする請求項 2 記載の半導体チップの実装方法。

【請求項 6】 熱硬化性接着剤(3)を用いて半導体チップ(4)を基板(1)にフェイスダウン実装するに際し、該接着剤(3)の硬化温度に半導体チップ(4)を加熱する前記加熱圧着ヘッド(6)の下面には該半導体チップ(4)を吸着する手段が設けられ、該半導体チップ(4)の前記実装部周囲に当接する前記加熱体(11, 31)が上下動自在手段(12)を介して該加熱圧着ヘッド(6)より垂下し、該加熱圧着ヘッド(6)に吸着した該半導体チップ(4)が該接着剤(3)に接する前に該加熱体(11)が該基板(1)に当接するように構成したこと、を特徴とする半導体チップの実装装置。

【発明の詳細な説明】

【0001】

【産業上の利用分野】 本発明は半導体チップの実装方法とそのための実装装置、特に、加熱圧着ヘッドを用いてガラス基板に半導体チップを実装させる方法とその装置に関する。

【0002】 製品サイズの小型、薄型化に伴って、その製品に使用する半導体チップの実装方法、例えば一対のガラス基板の間に液晶を充填した液晶表示パネルにパネル駆動用の半導体チップを実装する方法として、チップ・オン・グラス(COG)方式、即ち、接着剤を用い半導体チップをパネル構成済みのガラス基板にフェイスダ

ウン実装する方式、が採用されるようになった。

【0003】

【従来の技術】 図 7 は回路基板に半導体チップをフェイスダウン実装する従来方法の説明図である。

【0004】 熱硬化性接着剤を用いて半導体チップを回路基板にフェイスダウン実装する一般的な方法を示す図 7(a)～(c)において、多数の導体端子 2 を形成した回路基板 1 の表面に接着剤 3 を貼着し、半導体チップ 4 に形成した多数のバンプ 5 と端子 2 との位置合わせをしたのち、加熱圧着ヘッド 6 を用いて半導体チップ 4 のバンプ 5 を回路基板 1 の端子 2 に押圧し、20～30秒程度経過し接着剤 3 が硬化した状態でヘッド 6 を除去し、半導体チップ 4 のフェイスダウン実装が完了する。

【0005】かかる半導体チップ 4 の実装方法において、半導体チップ 4 の本体より突出するバンプ 5 と、回路基板 1 の半導体チップ搭載面より突出する端子 2 との間の接着剤 3 は、半導体チップ 4 の押圧力によって押し退けられ、硬化した接着剤 3 はバンプ 5 と端子 2 との電気的接続を維持すると共に、回路基板 1 と半導体チップ 4 との機械的接合を維持する。

【0006】一般に接着剤 3 は熱硬化性であり、例えればエポキシ系の熱硬化性接着剤 3 は170℃～200℃程度で硬化させる。そのとき回路基板 1 は、半導体チップ 4→バンプ 5→端子 2 および半導体チップ 4→接着剤 3 を介して加熱される。

【0007】

【発明が解決しようとする課題】 半導体チップ 4 の前記実装方法において、回路基板 1 がガラス基板であり、そのガラス基板 1 が液晶表示パネルを構成したものであるとき、表示パネルに充填した液晶およびガラス基板 1 に被着した偏光膜は200℃以上に加熱できない。従つて、半導体チップ 4 をフェイスダウン実装するとき、ガラス基板 1 は室温であった。

【0008】そのため、半導体チップ 4 を介して20秒～30秒程度の加熱で硬化する接着剤 3 は、半導体チップ 4 に接する部分とガラス基板 1 に接する部分との間に温度差が発生する。

【0009】半導体チップ 4 の実装時における接着剤 3 の温度差を示す図 8において、実線 A は半導体チップ 4 に接する部分の温度上昇特性、実線 B はガラス基板 1 に接する部分の温度上昇特性であり、30秒程度経過し接着剤 3 が硬化した時点で、基板 1 に接する部分の接着剤 3 の温度は、半導体チップ 4 に接する部分より20～30℃程度低くなる。

【0010】接着剤 3 の前記温度差は、接着剤 3 の流動性むら、硬化進行むらを招き、その結果図 9 に示す如く、硬化を完了した接着剤 3 の内部に気泡 7 が生じ易い。かかる気泡 7 は、半導体チップ 4 の実装に対する信頼性を低下せしめ、加速試験時に半導体チップ 4 の接続断の要因になった。

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【0011】なお、液晶表示パネルに実装する半導体チップ4の大きさは、現在、 $3\text{mm} \times 1.7\text{mm} \sim 1\text{mm} \times 10\text{mm}$ 程度であり、小型の半導体チップ4を使用した接着剤3の前記温度差は比較的小さくなるが、気泡7をなくすことができない。

【0012】

【課題を解決するための手段】図1は本発明の基本構成の説明図である。熱硬化性接着剤を用いて半導体チップ4を基板にフェイスダウン実装したとき、硬化した接着剤内に気泡が生じないようにする本発明方法は、図1

(a)に示す如く、半導体チップ実装用の導体端子2を形成し半導体チップ実装用の熱硬化性接着剤3を貼着したガラス基板1の局部、即ち半導体チップ実装部の周辺部を加熱体11で加熱し、ガラス基板1を介して接着剤3の予備加熱を行なう。

【0013】次いで、半導体チップ4に形成したバンプ5と端子2との位置合わせを行なったのち、図1(b)に示す如く、半導体チップ4を下面に吸着し半導体チップ4を接着剤3の硬化温度に加熱する加熱圧着ヘッド6を使用し、半導体チップ4のバンプ5を基板1の端子2に押圧する。

【0014】そこで、接着剤3が硬化してからヘッド6を除くと、図1(c)に示す如く半導体チップ4のフェイスダウン実装が完了する。基板1の予備加熱体11を具えた半導体チップ実装装置要部の基本構成例は図1

(d)に示す如く、下面に半導体チップ4を吸着する加熱圧着ヘッド6から、上下動自在手段(例えばばね)12を介して基板予備加熱体11が垂下し、加圧シリンダ10の動作によってヘッド6を降下させたとき、ヘッド6によって加熱された加熱体11が基板1に接したのち、ヘッド6によって加熱された半導体チップ4が接着剤3に接するように、加熱体11の垂下量を設定する。

【0015】

【作用】以上説明したように本発明は、加熱体11が基板1の半導体チップ実装部(所要局部)を加熱し、局部加熱された基板1が接着剤3を予備加熱したのち、半導体チップ4が接着剤3をその硬化温度に加熱する。

【0016】従って、全体を加熱できない基板1例えば液晶表示パネルを構成するガラス基板1に半導体チップ4をフェイスダウン実装するとき、接着剤3の硬化時ににおける温度差を抑制し、従来方法において発生した気泡7をなくすことができるようになる。

【0017】その結果、半導体チップ4のフェイスダウン実装に対し、高い信頼性を確保できるようになった。

【0018】

【実施例】図2は本発明の第1の実施例における装置の主要構成の説明図、図3は図2に示す装置の動作説明図、図4は本発明の第2の実施例における装置の主要構成の説明図、図5は本発明の第3の実施例に係わる基板予備加熱体の説明図、図6は本発明の第4の実施例にお

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ける装置の主要構成の説明図である。

【0019】図2において、上下動する加圧シリンダ10の下面にはヒータを内蔵した加熱圧着ヘッド6を装着する。ヘッド6の下面には、半導体チップ4を真空吸着する吸気孔(吸着手段)が開口し、ヘッド6の側面には半導体チップ4を囲う枠型の基板予備加熱用ヘッド14を設ける。

【0020】予備加熱用ヘッド14は、断面がほぼコ字形即ち下向き開口が窄まるコ字形断面の枠型基体15、

10 上部が基体15のコ字形内に嵌合し下端部が基体15より突出する基板加熱体16、基体15のコ字形内に収納され加熱体16を常時下方に付勢する耐熱性弾性ゴム(上下動自在手段)17からなる。なお、弾性ゴム17は他の弹性体例えばコイルばねに置き換える可能である。

【0021】図1の加熱体11に相当しヒータを内蔵しない加熱体16は、基体15を介して加熱圧着ヘッド6により、例えば150°C程度に加熱されるようになる。図3において、多数の導体端子2を形成したガラス基板1の表面に、シート状の接着剤3を貼着し(8.0°C、1秒程度)、半導体チップ4に形成した多数のバンプ5と基板端子2との位置合わせをしたのち、加圧シリンダ10の降下動によって、加熱された基板加熱体16の下面を基板1の表面に数十秒程度だけ接触させる。

【0022】すると、加熱体16の熱が基板1に伝播し、基板1の半導体チップ実装部を接着剤3の硬化が著しく進まない程度の適当な温度、例えば120°C程度に加熱されるようになる。

【0023】次いで、弾性ゴム17が圧縮されるように加圧シリンダ10をさらに降下せしめ、バンプ5が端子2に当接した状態で数十秒程度維持せしめ接着剤3を硬化させたのち、ヘッド6の半導体チップ吸着力を解除し加圧シリンダ10を上昇させると、接着剤3内に気泡を発生させることなく、半導体チップ4のフェイスダウン実装が完了する。

【0024】図4において、下面に半導体チップ4を真空吸着する加熱圧着ヘッド6の側面には、図1の加熱体11に相当する金具(加熱体)21が垂下する。そして、上下方向に長さを有する長孔22が設けられた金具21は、長孔(上下動自在手段)22が上下動自在に嵌合するねじ23によってヘッド6の側面に垂下し、それ自体の重量によって長孔22の長さだけ上下動自在である。

【0025】そして、圧着ヘッド6によって加熱される金具21は加熱体16と同様に、接着剤3に気泡を生じない半導体チップ4のフェイスダウン実装を可能にする。図5において、本発明方法に係わるガラス基板局部加熱用シート(加熱体)31は、通電による発熱シートを一対の絶縁フィルムで挟んだものであり、ガラス基板1の半導体チップ実装部に搭載し使用する。

【0026】図6の実施例は、シート31を圧着ヘッド

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6に装着し、シート31を上下動自在かつヘッド6の上下動によって、基板1を介する接着剤3を予備加熱し、半導体チップ4をフェイスダウン実装するようにした構成例である。

【0027】圧着ヘッド6の側面より垂下し上下動自在な一对の金具21にシート31を装着した実施例である。図6の装置は、加熱圧着ヘッド6からの熱伝動を利用して加熱体1-6または金具21により基板1の貼着接着剤3を予備加熱する方式より、接着剤3の予備加熱効率がよい、即ち予備加熱の所要時間を短縮できる。

【0028】

【発明の効果】以上説明したように本発明は、基板に半導体チップをフェイスダウン実装した接着剤の気泡をなくし、フェイスダウン実装に対し高い信頼性を確保できるようにした。

【図面の簡単な説明】

【図1】 本発明の基本構成の説明図

【図2】 本発明の第1の実施例における装置の主要構成の説明図

【図3】 図2に示す装置の動作説明図

【図4】 本発明の第2の実施例における装置の主要構成の説明図

【図5】 本発明の第3の実施例に係わる基板予備加熱

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体の説明図

【図6】 本発明の第4の実施例における装置の主要構成の説明図

【図7】 半導体チップをフェイスダウン実装する従来方法の説明図

【図8】 従来方法による半導体チップ実装時の接着剤の温度分布図

【図9】 従来方法による接着剤内部に発生する気泡の説明図

10 【符号の説明】

1 半導体チップ実装基板

2 導体端子

3 熱硬化性接着剤

4 半導体チップ

5 バンプ

6 加熱圧着ヘッド

11, 16 基板局部を加熱する加熱体

12 上下動自在手段

17 弾性ゴム(上下動自在手段)

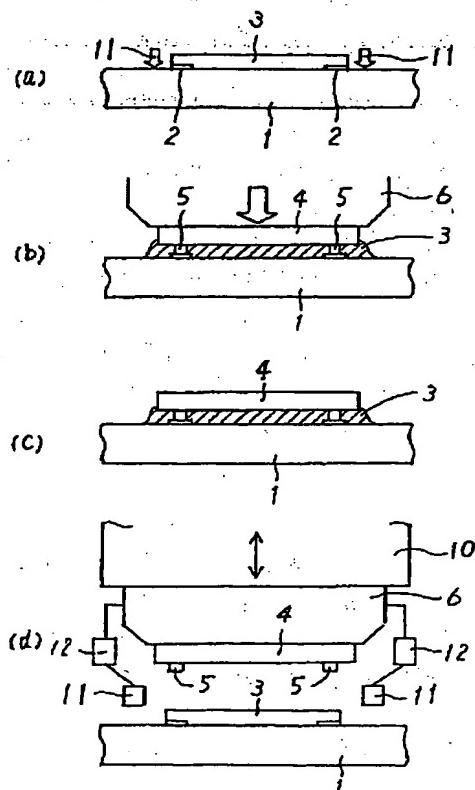
20 21 金具(加熱体)

22 長孔(上下動自在手段)

31 加熱シート(加熱体)

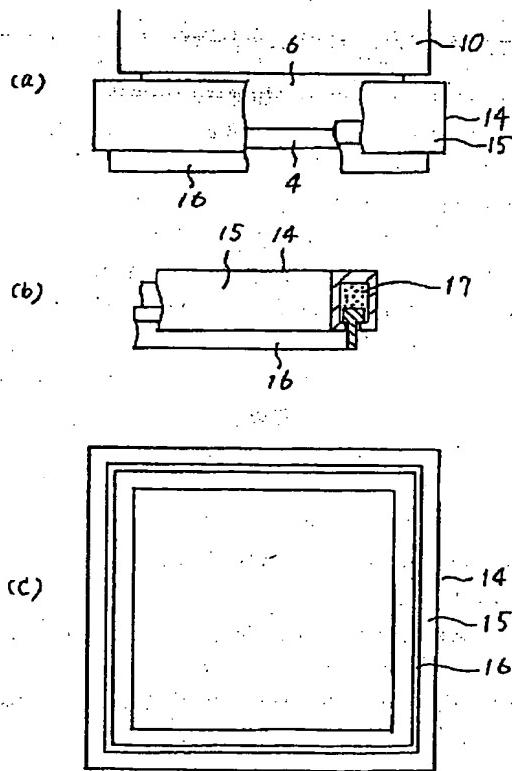
【図1】

本発明の基本構成の説明図



【図2】

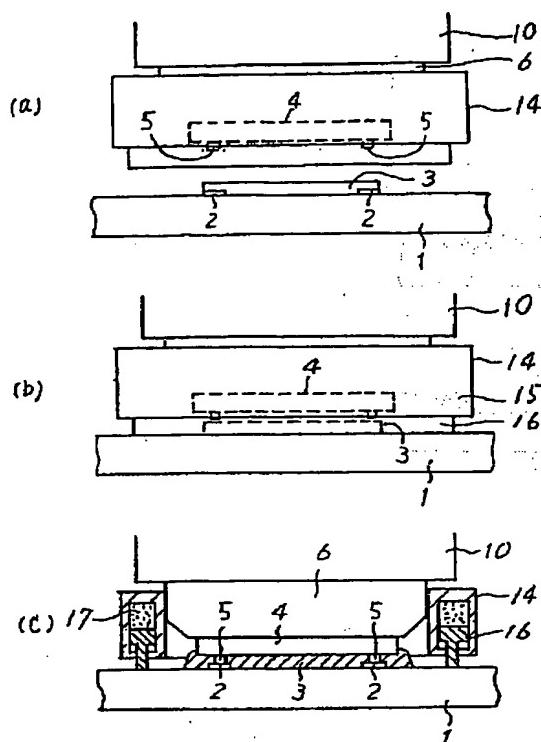
本発明の第1の実施例における装置の主要構成の説明図



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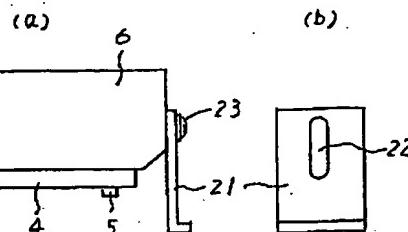
【図3】

図2に示す装置の動作説明図



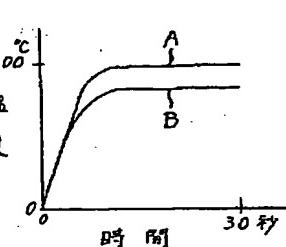
【図4】

本発明の第2の実施例における装置の主要構成の説明図



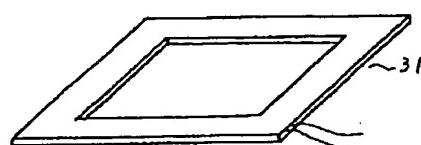
【図8】

従来方法による半導体チップ実装時の接着剤の温度分布図



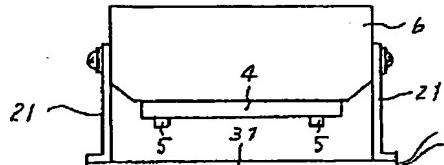
【図5】

本発明の第3の実施例に係る基板予備加熱体の説明図



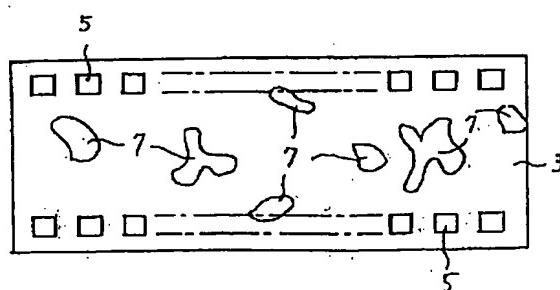
【図6】

本発明の第4の実施例における装置の主要構成の説明図



【図9】

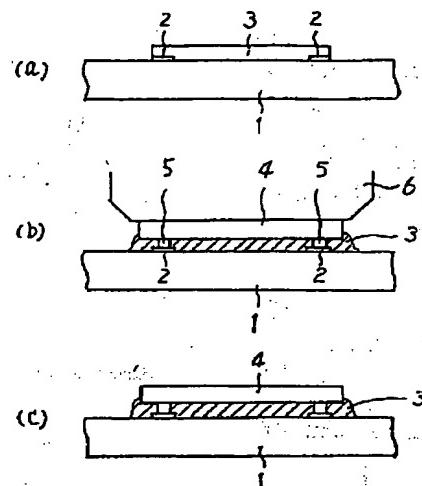
従来方法による接着剤内部に発生する気泡の説明図



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【図7】

半導体チップをフェイスダウン実装する従来方法の説明図



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